## Digital Input/Output Interface Card "OPC-DIO"

Thank you for purchasing the digital input/output interface card "OPC-DIO". By installing digital input/output interface card "OPC-DIO" on the FRENIC series, frequency settings can be specified with binary codes and BCD codes, output frequency and so on can be monitored with binary codes, and general-purpose input/output terminals can be expanded.

1. Applicable Inverters

Table 1 shows applicable inverter series.
Table 1 Applicable Inverter and ROM Version

| Series | Inverter type | Inverter capacity | ROM version |
| :---: | :---: | :---: | :---: |
| FRENIC-Ace | FRN $\square \square \square E 2 \square-\square \square$ | Full capacity | 0300 or later |

## 2. Product Check

Check the following items.
(1) Ensure that the package contains the DIO interface card and 2 screws (M3 x 8).
(2) Ensure that no damage such as abnormalities, dents, or bending has occurred to the parts on the DIO interface card during transport.
(3) Ensure that the "OPC-DIO" model name is stamped on the top of the DIO interface card. (Fig. 2-1)
If you suspect the product is not working properly or if you have any questions about your product, contact the shop where you bought the product or your local Fuji branch office.

## 3. Basic Connection Drawing




Fig. 2-1 Front of Card
Fig. 2-2 Back of Card
4. Electrical Specifications

Table 4-1 Electrical Specifications List

| Terminal symbol | Item |  | Specification |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Max. |
| I1-I13 | Operating voltage (SINK) | ON level | 0 V | 2 V |
|  |  | OFF level | 22 V | 27 V |
|  | Operating voltage (SOURCE) | ON level | 22 V | 27 V |
|  |  | OFF level | 0 V | 2 V |
|  | Operating current at ON <br> (when input voltage 0 V ) |  | 2.5 mA | 5 mA |
|  | Allowable leakage current at OFF |  | - | 0.5 mA |
| O1-08 | Operating voltage | ON level | - | 2 V |
|  |  | OFF level | - | 27 V |
|  | Maximum current at ON |  | - | 50 mA |
|  | Leakage current at OFF |  | - | 0.1 mA |

Fig. 3-1 Basic Connection Drawing



Input terminals

Fig. 5-1 Connection Terminal Allocation Drawing
6. Terminal Functions

| Table 5-1 Terminal Specifications |  |
| :--- | :---: |
| Terminal size | M 2 |
| Tightening torque (N•m) | $0.19 \pm 10 \%$ |
| Recommended wire size * | AWG22-24 |
| Stripped wire length (mm) | 5 |
| *An insulated wire with allowable <br> temperature of $105^{\circ} \mathrm{C}$ (UL compliant <br> product) is recommended. |  |

Table 6-1 shows a list of terminal functions.
Table 6-1 Terminal Function List

| Terminal symbol | Terminal name | Terminal function description |
| :---: | :---: | :---: |
| I1-I13 | Digital input 1 to 13 | (1) Frequency can be set with a setting method for all frequencies set with function code o20. In this case, terminal $I 13$ is a hold input signal. Furthermore, frequency settings can be given polarity with o19. Refer to the respective inverter user's manuals for details. <br> (2) These terminals can be used as general-purpose input terminals (inverter unit $X$ terminal or equivalent). Functions can be selected from o101 to o113. Assign input signals requiring responsiveness to the inverter unit side X terminal. <br> (3) SW11 can be used to switch between the SINK and SOURCE methods. <br> The minimum permissible variation width of input terminal signals is 2 [ ms ] or higher. |
| M1 | External power supply input | This is a power supply terminal used for external power supplies (+22 to +27 VDC). |
| CM | Digital common | This is a common terminal for digital input signals. It has the same electric potential as inverter unit terminal CM . |
| O1-08 | Transistor output 1 to 8 | (1) All types of monitor data (output frequency, output current, etc) set with function code o21 can be output as 8-bit binary output signals. <br> (2) When function code o21 is set to 99 , all output terminals can be used as general-purpose output terminals (inverter unit Y terminal or equivalent). <br> Functions can be selected from o01 to o08. <br> Refer to the respective inverter user's manuals for details. Assign output signals requiring responsiveness to the inverter unit side Y terminal. |
| M2 | Transistor output common | This is a common terminal for transistor output signals. Inverter unit terminals CM, 11, and CMY are insulated. |

7. Option Communication Error (

This error occurs when a communication error occurs between the digital I/O card and inverter unit.

| Cause | Check and countermeasure |
| :--- | :--- |
| (1) There is a problem with the <br> connection between the digital I/O <br> card and inverter unit. | Check whether the option connection cable between the <br> digital I/O card and inverter unit is connected properly, and <br> ensure that the cable fits securely into the adapter <br> connector. <br> $\rightarrow$ Connect the option connection cable properly. <br> Insert securely into the adapter connector. |
| (2) Influenced by strong noise from <br> surroundings | Check noise countermeasures (connection status, signal <br> wire and communication cable/main circuit wiring <br> installation, etc.) <br> $\rightarrow$ Improve noise countermeasures. |

Note: There may also be a problem with the connection between the digital I/O card and inverter unit (I-イappear. In this case also, check whether the option connection cable is connected properly.

## 8. I/O Interface

## 8 8-1. Input Interface

8-1-1. Switching Between Digital Input Terminal (I1-I13) SINK/SOURCE Methods
Switching between input terminal (I1-I13) SINK and SOURCE methods is possible with switch SW11.
Refer to Table 8-1 and set the input method
Table 8-1 Switch SW11 Setting

| Input method | Switch setting |
| :---: | :---: |
| SINK method (default) | SINK <br> Switches to SINK side. $\square$ SOURCE |
| SOURCE method | Switches to  <br> SOURCE side. SW11SINK <br> SOURCE |

Not It is also possible to switch between the SINK and SOURCE methods for inverter unit digital input terminals. Set the SINK/SOURCE method setting the same as that for the inverter unit.

8-1-2. Connecting with Digital Input Terminal (I1-I13) SINK/SOURCE Methods The digital input interface circuit connection method is shown in Table 8-2.

Table 8-2 Input Interface Circuit Connection Method

| Power supply | Connection method |  |
| :---: | :---: | :---: |
|  | SINK method | SOURCE method |
| Internal |  |  |
| External |  |  |

## 8-1-3. Contact Use Precaution

If configuring input circuits with contacts, use contacts that don't cause contact defects (contacts with high contact reliability).

## 8-2. Output Interface

The output interface circuit connection method is shown in Table 8-3.
Table 8-3 Output Interface Circuit Connection Method

9. I/O Check

The I/O status of external signals can be displayed on the LED monitor with program mode menu No. 4 "I/O Check" on the keypad. ${ }^{(*)}$
The digital interface option I/O signal status can be displayed with an "LED segment ON/OFF indication" and "hexadecimal indication".
Assignment of each signal is as follows.
(*) Refer to the inverter unit instruction manual for details on menu No. 4 "I/O Check".


Table 9-2 Hexadecimal Indication with 7 Segment LED

| LED No. | LED4 |  |  |  | LED3 |  |  |  | LED2 |  |  |  | LED1 |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Bit | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| Input terminal | - | - | - | 113 | 112 | 111 | 110 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 |
| Output terminal | - | - | - | - | - | - | - | - | 08 | 07 | 06 | O5 | O4 | O3 | O2 | O1 |

## 10. Function Code Settings

To enable frequency setting input from this interface card, it is necessary to set "11" (digital input interface) for function code F01 (frequency setting 1) or C30 (frequency setting 2). Furthermore, the frequency setting polarity and input mode are set with option function code o20 (DI mode selection).
Monitor items assigned to digital output signals for this interface card is set with option function code o21 (DO mode selection).

10-1. Function Code List

## FRENIC-Ace series

| Function code | Function code details |  | Data | Data content |  | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{gathered} \text { F01 } \\ \text { (C30) } \end{gathered}$ | Frequency selection 1 (Frequency selection 2) |  | 11 | Frequency setting with DIO option |  |  |
|  |  |  | Other than 11 | Frequency setting corresponding to each data item |  |  |
| 020 | Input mode selection |  | 0 | 8-bit binary setting |  | Note: 113 is a dedicated terminal for hold signals. <br> - Input data ( 11 to I 12 ) is received when $\mathrm{I} 13=0$. <br> - Input data (I1 to I12) is not received when I13=1. |
|  |  |  | 1 | 12-bit binary setting |  |  |
|  |  |  | 4 | BCD 3 digit setting 0 to 99.9 |  |  |
|  |  |  | 5 | BCD 3 digit setting 0 to 500 |  |  |
|  |  |  | 99 | General-purpose input processing |  | General-purpose input functions can be selected with o101 to o113. |
| -0101-0113 | Terminal I1 to 113 function selection |  | Same as inverter unit function code E01 |  |  | Valid when o20=99 |
| Function code | Function code details | Data | Monitor content |  | Remarks |  |
| 021 | Output mode selection | 0 | Outp (befo com | ut frequency re slide ensation) | 100\%/8-bit | (Output frequency/max. output frequency) $\times 255$ |
|  |  | 1 | Outp (afte com | ut frequency slide ensation) | 100\%/8-bit | (Output frequency/max. output frequency) $\times 255$ |
|  |  | 2 | Outp | ut current | 200\%/8-bit | (Output current/(inverter rated output current $\times 2$ ) $\times 255$ |
|  |  | 3 | Outp | ut voltage | 100\%/8-bit | (Output voltage/250 V) x 255: 200 V series (Output voltage/500V) $\times 255: 400 \mathrm{~V}$ series |
|  |  | 4 | Outp | ut torque | 200\%/8-bit | (Output torque/(motor rated torque $\times 2$ )) $\times 255$ |
|  |  | 5 | Load | factor | 200\%/8-bit | (Load factor/(motor rated load $\times 2$ ) $\times 255$ |
|  |  | 6 | Powe | r consumption | 200\%/8-bit | (Power consumption/(inverter rated output $\times 2) \times 255$ |
|  |  | 7 | $\begin{aligned} & \hline \text { PID } \\ & \text { (PV) } \end{aligned}$ | feedback value | 100\%/8-bit | (PID feedback value/100\% of feedback value) $\times 255$ |
|  |  | 8 | Dete spee | cted d/estimated speed | 100\%/8-bit | (PG feedback frequency/max. output frequency) $\times 255$ |
|  |  | 9 | Inter volta | mediate DC circuit ge | 100\%/8-bit | 200 V series: (Intermediate DC circuit voltage/500 [V]) x 255 <br> 400 V series: (Intermediate DC circuit voltage/1000 [V]) x 255 |
|  |  | 13 | Moto | r output | 200\%/8-bit | (Motor output/(motor rated output $\times 2$ )) $\times 255$ |
|  |  | 15 | PID | command (SV) | 100\%/8-bit | (PID command/100\% of feedback value) $\times 255$ |
|  |  | 16 | PID | output (MV) | 100\%/8-bit | (PID output/max. output frequency) $\times 255$ |
|  |  | 17 | Sync devia | hronous angle ation | $\pm 180$ deg./7-bit | ( $\pm$ synchronous angle deviation/180 deg.) $\times 127$ |
|  |  | 18 | Inver temp | ter cooling fin erature | $200^{\circ} \mathrm{C} / 8$-bit | (Inverter cooling fin temperature/200 ${ }^{\circ} \mathrm{C}$ ) $\times 255$ |
|  |  | 21 | PG f | eedback value (PV) | 100\%/8-bit | (PG feedback frequency/max. output frequency) $\times 255$ |
|  |  | 99 | Indiv | dual signal output | General-purpose output functions can be selected with o01 to o08. |  |
| o01 to 08 | Terminal 01 to 08 function selection | Same as inverter unit function code E20 |  |  | Valid when o21 = 99 |  |

[^0]11. Detailed Function Description

A detailed function description when each function code is set is shown in Table 11-1, Table 11-2, and Table 11-3.
Table 11-1 Detailed Function Description (Input)


Table 11-2 Detailed Function Description (Output)


Table 11-3 Detailed Function Description (Output)


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[^0]:    If the monitor amount exceeds $100 \%$, the 8 -bit maximum output is "11111111(255)".

