

Digital Input/Output Interface Card "OPC-DIO"

Thank you for purchasing the digital input/output interface card "OPC-DIO". By installing digital input/output interface card "OPC-DIO" on the FRENIC series, frequency settings can be specified with binary codes and BCD codes, output frequency and so on can be monitored with binary codes, and general-purpose input/output terminals can be expanded.

1. Applicable Inverters

Table 1 shows applicable inverter series.

Table 1 Applicable Inverter and ROM Version

Series	Inverter type	Inverter capacity	ROM version
FRENIC-Ace	FRN000E20-000	Full capacity	0300 or later

2. Product Check

Check the following items.

- (1) Ensure that the package contains the DIO interface card and 2 screws (M3 x 8).
- (2) Ensure that no damage such as abnormalities, dents, or bending has occurred to the parts on the DIO interface card during transport.
- (3) Ensure that the "OPC-DIO" model name is stamped on the top of the DIO interface card. (Fig. 2-1)

If you suspect the product is not working properly or if you have any questions about your product, contact the shop where you bought the product or your local Fuji branch office.

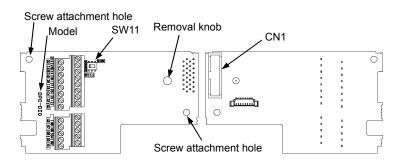
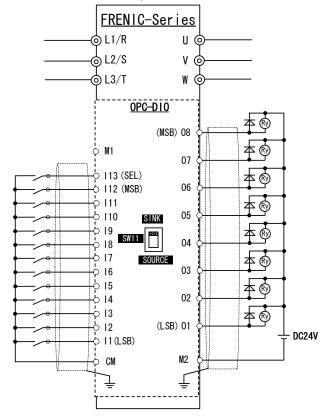


Fig. 2-1 Front of Card

Fig. 2-2 Back of Card

3. Basic Connection Drawing



4. Electrical Specifications

Table 4-1 Electrical Specifications List

Terminal	Ite	Specifi	cation	
symbol	ite	:111	Min.	Max.
	Operating	ON level	0 V	2 V
	voltage (SINK)	OFF level	22 V	27 V
	Operating	ON level	22 V	27 V
I1 - I13	voltage (SOURCE)	OFF level	0 V	2 V
	Operating curre (when input vo	2.5mA	5mA	
	Allowable leak OFF	-	0.5mA	
	Operating	ON level	-	2 V
O1 - O8	voltage	OFF level	-	27 V
01-06	Maximum curre	Maximum current at ON		
	Leakage curre	nt at OFF	-	0.1mA

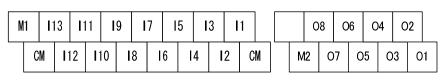
Fig. 3-1 Basic Connection Drawing

⚠ WARNING

Before starting installation and wiring, turn OFF the power and wait at least ten minutes. Mace sure that the LED monitor and charging lamp are turned OFF. Further, make sure, using a multimeter or a similar instrument, that the DC link bus voltage between the terminals P(+) and N(-) has dropped to the safe level (+25 VDC or below)

Otherwise, electric shock could occur.

5. Terminal Allocation



Input terminals

Output terminals

Fig. 5-1 Connection Terminal Allocation Drawing

Table 5-1 Terminal Specifications					
Terminal size	M2				
Tightening torque (N·m)	0.19 ±10%				
Recommended wire size *	AWG22 – 24				
Stripped wire length (mm)	5				

^{*} An insulated wire with allowable temperature of 105 °C (UL compliant product) is recommended.

6. Terminal Functions

Table 6-1 shows a list of terminal functions.

Table 6-1 Terminal Function List

Terminal symbol	Terminal name	Terminal function description
I1 - I13	Digital input 1 to 13	 (1) Frequency can be set with a setting method for all frequencies set with function code o20. In this case, terminal I13 is a hold input signal. Furthermore, frequency settings can be given polarity with o19. Refer to the respective inverter user's manuals for details. (2) These terminals can be used as general-purpose input terminals (inverter unit X terminal or equivalent). Functions can be selected from o101 to o113. Assign input signals requiring responsiveness to the inverter unit side X terminal. (3) SW11 can be used to switch between the SINK and SOURCE methods. The minimum permissible variation width of input terminal signals is 2 [ms] or higher.
M1	External power supply input	This is a power supply terminal used for external power supplies (+22 to +27 VDC).
СМ	Digital common	This is a common terminal for digital input signals. It has the same electric potential as inverter unit terminal CM.
O1 - O8	Transistor output 1 to 8	 (1) All types of monitor data (output frequency, output current, etc) set with function code o21 can be output as 8-bit binary output signals. (2) When function code o21 is set to 99, all output terminals can be used as general-purpose output terminals (inverter unit Y terminal or equivalent). Functions can be selected from o01 to o08. Refer to the respective inverter user's manuals for details. Assign output signals requiring responsiveness to the inverter unit side Y terminal.
M2	Transistor output common	This is a common terminal for transistor output signals. Inverter unit terminals CM, 11, and CMY are insulated.

7. Option Communication Error (上ー与)

This error occurs when a communication error occurs between the digital I/O card and inverter unit.

Cause	Check and countermeasure
(1) There is a problem with the connection between the digital I/O card and inverter unit.	Check whether the option connection cable between the digital I/O card and inverter unit is connected properly, and ensure that the cable fits securely into the adapter connector. → Connect the option connection cable properly. Insert securely into the adapter connector.
(2) Influenced by strong noise from surroundings	Check noise countermeasures (connection status, signal wire and communication cable/main circuit wiring installation, etc.) → Improve noise countermeasures.

Note: There may also be a problem with the connection between the digital I/O card and inverter unit (- '- '- ' does not appear) when the code for function code o (option function) does not appear. In this case also, check whether the option connection cable is connected properly.

8. I/O Interface

8-1. Input Interface

8-1-1. Switching Between Digital Input Terminal (I1 - I13) SINK/SOURCE Methods
Switching between input terminal (I1 - I13) SINK and SOURCE methods is possible with switch SW11.
Refer to Table 8-1 and set the input method.

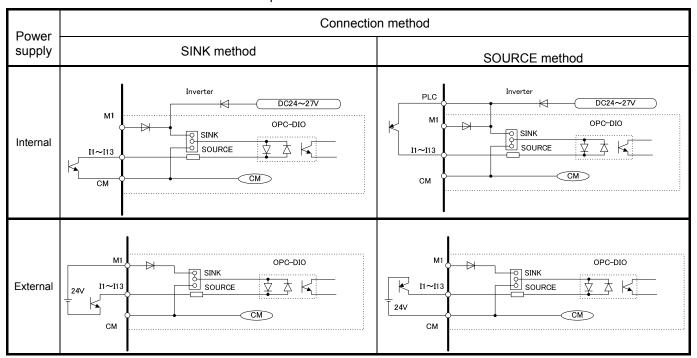
Table 8-1 Switch SW11 Setting

Input method	Switc	h setting
SINK method (default)	Switches to SINK side.	SW11 SINK SOURCE
SOURCE method	Switches to SOURCE side.	SW11 SINK SOURCE

Note input terminals. Set the SINK/SOURCE method setting the same as that for the inverter unit.

8-1-2. Connecting with Digital Input Terminal (I1 - I13) SINK/SOURCE Methods The digital input interface circuit connection method is shown in Table 8-2.

Table 8-2 Input Interface Circuit Connection Method



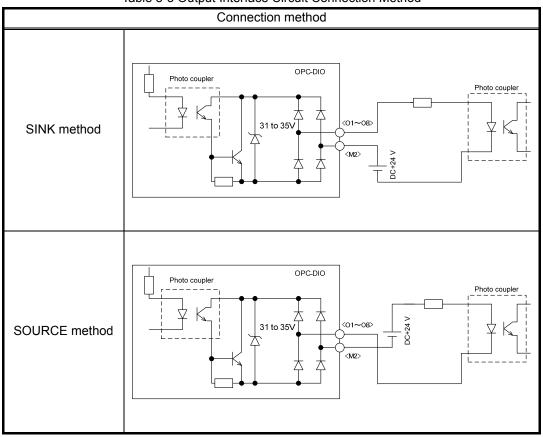
8-1-3. Contact Use Precaution

If configuring input circuits with contacts, use contacts that don't cause contact defects (contacts with high contact reliability).

8-2. Output Interface

The output interface circuit connection method is shown in Table 8-3.

Table 8-3 Output Interface Circuit Connection Method



9. I/O Check

The I/O status of external signals can be displayed on the LED monitor with program mode menu No.4 "I/O Check" on the keypad. (*)

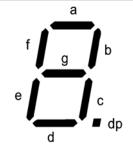
The digital interface option I/O signal status can be displayed with an "LED segment ON/OFF indication" and "hexadecimal indication".

Assignment of each signal is as follows.

(*) Refer to the inverter unit instruction manual for details on menu No.4 "I/O Check".

LED4 LED3 LED2 LED1





Segment	LED4	LED3	LED2	LED1
а	-	01	19	I1
b	-	02	I10	12
С	-	O3	I11	13
d	-	04	l12	14
е	-	O5	I13	15
f	-	O6		16
g	-	07		17
dp	-	O8		18

Table 9-1 LED Segment ON/OFF Indication

Table 9-2 Hexadecimal Indication with 7 Segment LED

LED No.		LE	D4			LE	D3			LE	D2			LE	D1	
Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Input	-	-	-	I13	l12	l11	I10	19	18	17	16	15	14	13	12	11
terminal																
Output	-	-	-	-	-	-	-	-	80	07	06	O5	04	О3	02	01
terminal																

10. Function Code Settings

To enable frequency setting input from this interface card, it is necessary to set "11" (digital input interface) for function code F01 (frequency setting 1) or C30 (frequency setting 2). Furthermore, the frequency setting polarity and input mode are set with option function code o20 (DI mode selection).

Monitor items assigned to digital output signals for this interface card is set with option function code o21 (DO mode selection).

10-1. Function Code List

FRENIC-Ace se	ries
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Function code	Function code details	е	Data	Data	content	Remarks	
F04	Frequency		11	Frequency setting v	vith DIO option		
F01 (C30)	selection 1 (Frequency selection 2)		Other than 11	Frequency setting of data item	corresponding to each		
o20	Input mode selection	Input mode		8-bit binary setting	l	Note: I13 is a dedicated terminal for hold signals. Input data (I1 to I12) is received when I13=0.	
			1	12-bit binary setting	J	• Input data (I1 to I12) is not received when I13=1.	
			4	BCD 3 digit setting			
		L	5	BCD 3 digit setting	0 to 500		
			99	General-purpose in	put processing	General-purpose input functions can be selected wit o101 to o113.	
o101 - o113	Terminal I1 to function selection	-	Same as	inverter unit function	code E01	Valid when o20=99	
Function code	Function code details	Dat	ta I	Monitor content		Remarks	
o21	Output mode selection	0	(befo	out frequency ore slide pensation)	100%/8-bit	(Output frequency/max. output frequency) x 255	
		1		out frequency r slide pensation)	100%/8-bit	(Output frequency/max. output frequency) x 255	
		2	Outp	ut current	200%/8-bit	(Output current/(inverter rated output current x 2) x 255	
		3	Outp	ut voltage	100%/8-bit	(Output voltage/250 V) x 255: 200 V series (Output voltage/500V) x 255: 400 V series	
		4	Outp	ut torque	200%/8-bit	(Output torque/(motor rated torque x 2)) x 255	
		5	Load	I factor	200%/8-bit	(Load factor/(motor rated load x 2)) x 255	
		6	Powe	er consumption	200%/8-bit	(Power consumption/(inverter rated output x 2) x 255	
		7	PID (PV)	feedback value	100%/8-bit	(PID feedback value/100% of feedback value) x 255	
		8	Dete spee	cted d/estimated speed	100%/8-bit	(PG feedback frequency/max. output frequency) x 255	
				9 Intermediate DC circuit		100%/8-bit	200 V series: (Intermediate DC circuit voltage/500 [V]) 255 400 V series: (Intermediate DC circuit voltage/1000 [V]) 255
		13		or output	200%/8-bit	(Motor output/(motor rated output x 2)) x 255	
		15		command (SV)	100%/8-bit	(PID command/100% of feedback value) x 255	
		16		output (MV)	100%/8-bit	(PID output/max. output frequency) x 255	
		17	, Synd	chronous angle ation	±180 deg./7-bit	(± synchronous angle deviation/180 deg.) x 127	
	1		Inver	rter cooling fin perature	200 °C/8-bit	(Inverter cooling fin temperature/200 °C) x 255	
		21	PG f	eedback value (PV)	100%/8-bit	(PG feedback frequency/max. output frequency) x 255	
		99	Indiv	idual signal output	General-purpose outp	out functions can be selected with o01 to o08.	
o01 to 08	Terminal 01 to 08 function selection		ne as inv e E20	verter unit function	Valid when o21 = 99		

A detailed function description when each function code is set is shown in Table 11-1, Table 11-2, and Table 11-3.

Table 11-1 Detailed Function Description (Input)

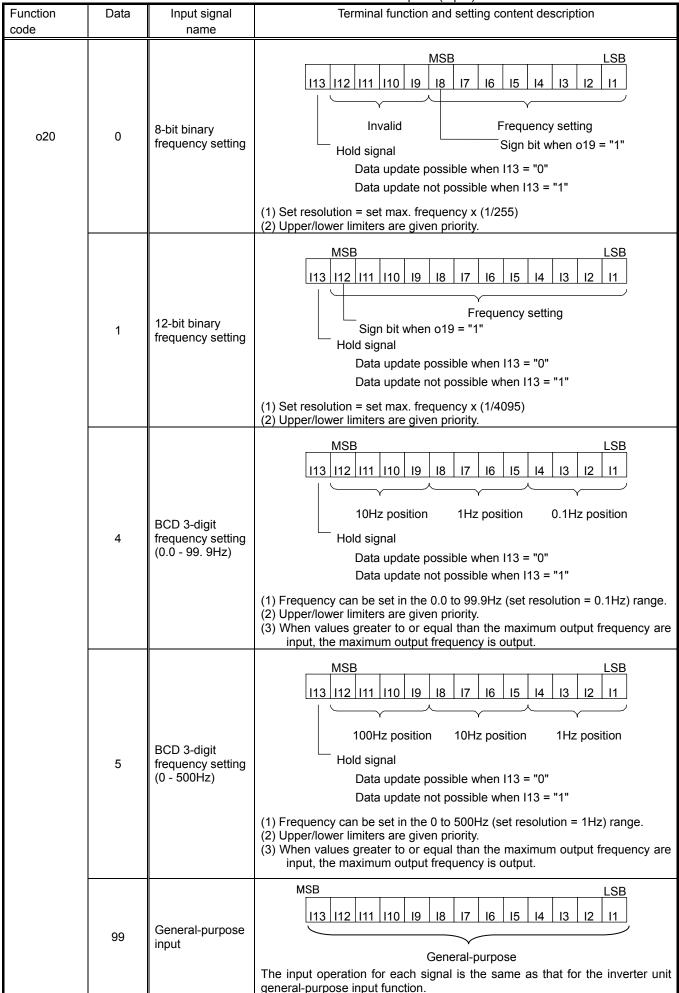


Table 11-2 Detailed Function Description (Output)

	T	Table 11-2 Detailed F	-unction Description (Output)				
Function code	Data	Output signal name	Terminal function and setting content description				
o21	0	Output frequency (before slide compensation)	MSB LSB 08 07 06 05 04 03 02 01 100% of max. output frequency / 8-bit				
	1	Output frequency (after slide compensation)	MSB LSB 08 07 06 05 04 03 02 01				
			100% of max. output frequency / 8-bit				
	2	Output current	MSB LSB 08 07 06 05 04 03 02 01				
			200% of inverter rated output current / 8-bit				
			MSB LSB				
	3 Output voltage		08 07 06 05 04 03 02 01				
	4 Output torque		100% of 250 V / 8-bit: 200 V series 100% of 500 V / 8-bit: 400 V series				
		Output torque	MSB LSB 08 07 06 05 04 03 02 01				
			200% of motor rated torque / 8-bit				
			MSB LSB				
	5	Load factor	08 07 06 05 04 03 02 01				
			200% of motor rated load / 8-bit				
	6	Power consumption	MSB LSB				
			08 07 06 05 04 03 02 01				
			200% of inverter rated output / 8-bit				
	7	PID feedback value (PV)	MSB LSB				
			08 07 06 05 04 03 02 01				
		Data stad an and/action stad	100% of feedback value / 8-bit				
	8	Detected speed/estimated speed	MSB LSB 08 07 06 05 04 03 02 01				
			00 07 00 00 04 00 02 01				
			100% of max. output frequency / 8-bit				

Table 11-3 Detailed Function Description (Output)

		Table 11-3 Deta	illed Function Description (Output)
Function code	Data	Output signal name	Terminal function and setting content description
o21	9	Intermediate DC circuit voltage	MSB LSB 08 07 06 05 04 03 02 01 100% of 500 V / 8-bit: 200 V series 100% of 1000 V / 8-bit: 400 V series
	13	Motor output	MSB LSB 08 07 06 05 04 03 02 01 200% of motor rated output / 8-bit
	15	PID command (SV)	MSB LSB 08 07 06 05 04 03 02 01 100% of feedback value / 8-bit
	16	PID output (MV)	MSB LSB 08 07 06 05 04 03 02 01 100% of max. output frequency / 8-bit
	17	Synchronous angle deviation	MSB LSB 08 07 06 05 04 03 02 01 100% of deviation -180 deg. to 180 deg. / 8-bit
	18	Inverter cooling fin temperature	MSB LSB 08 07 06 05 04 03 02 01 100% of 200 °C / 8-bit
	21	PG feedback value	MSB LSB 08 07 06 05 04 03 02 01 100% of max. output frequency / 8-bit
	99	Individual signal output	O8 O7 O6 O5 O4 O3 O2 O1 RUN: Running FDT: Frequency detection FAR: Frequency reached LU: Insufficient voltage stoppage IOL: Inverter output restricted IPF: Restoring power None: No function The default settings are shown above. The output operation for each signal is the same as that for the inverter unit general-purpose output function.

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